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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants   : Daniel B. Penney and Steven So   Attorney Docket No.: 500794.01

Patent No.   : US 7,003,643 B1

Issued   : February 21, 2006

Title   : BURST COUNTER CONTROLLER AND METHOD IN A MEMORY DEVICE
                 OPERABLE IN A 2-BIT PREFETCH MODE

NOTIFICATION OF ERRORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One or more of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicants request that this notification be placed in the Patent and Trademark Office file.

Column, Line	Reads	Should Read
Column 2, Line 67	"generated by an incrementing a burst"	--generated by incrementing a burst--
Column 6, Line 32	"it can be see"	--it can be seen--
Column 8, Line 55	"the coumter""	--the counter--

Column 10, Line 41 "at least one data storage --at least one data storage
         devices"   device--

Column 10, Line 6 "a dynamic random access --a dynamic random access
         memory, comprising" memory, comprising:--

Column 11, Line 39 "at least one data storage --at least one data storage
         devices"   device--

Column 11, Line 42 "a dynamic random access --a dynamic random access
         memory, comprising" memory, comprising:--

Respectfully submitted,

Date: _Jan. 22, 2008_   By: _Edward W. Bulchis_
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EWB:tdp

Enclosure:
   Postcard

500794.01 notification of errors